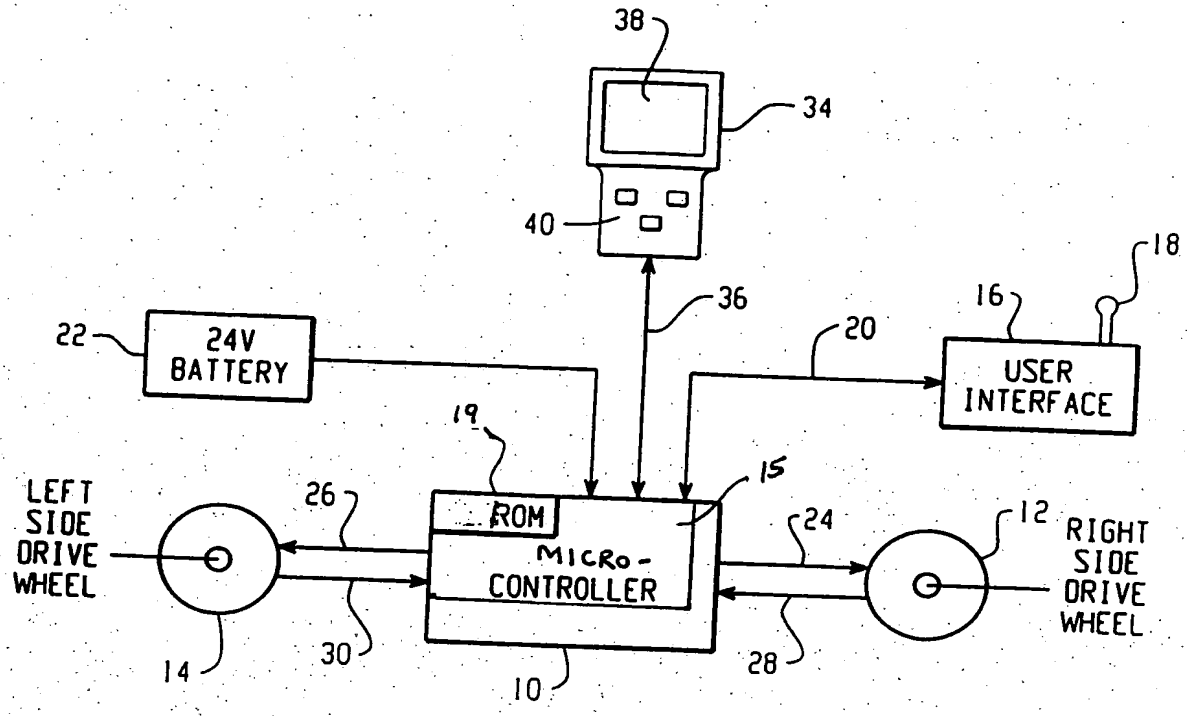

Fig. 1
(BACKGROUND)

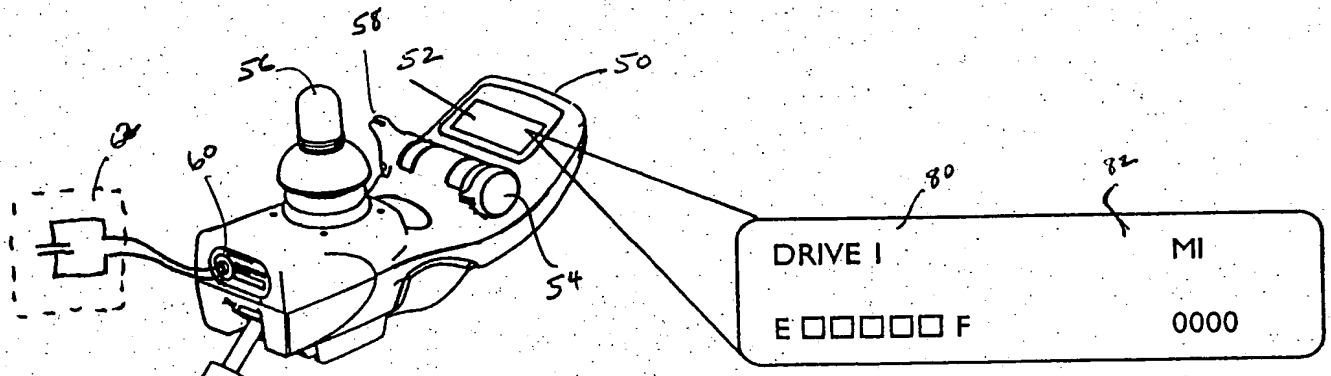

FIG. 2A

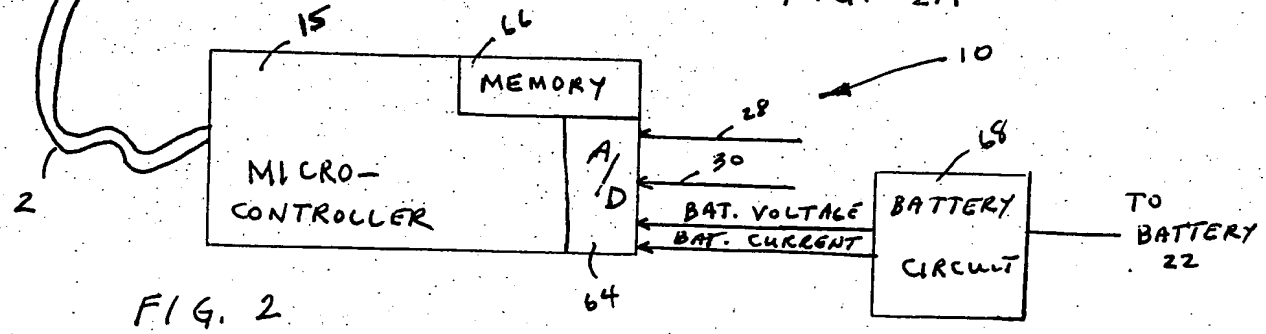

FIG. 2

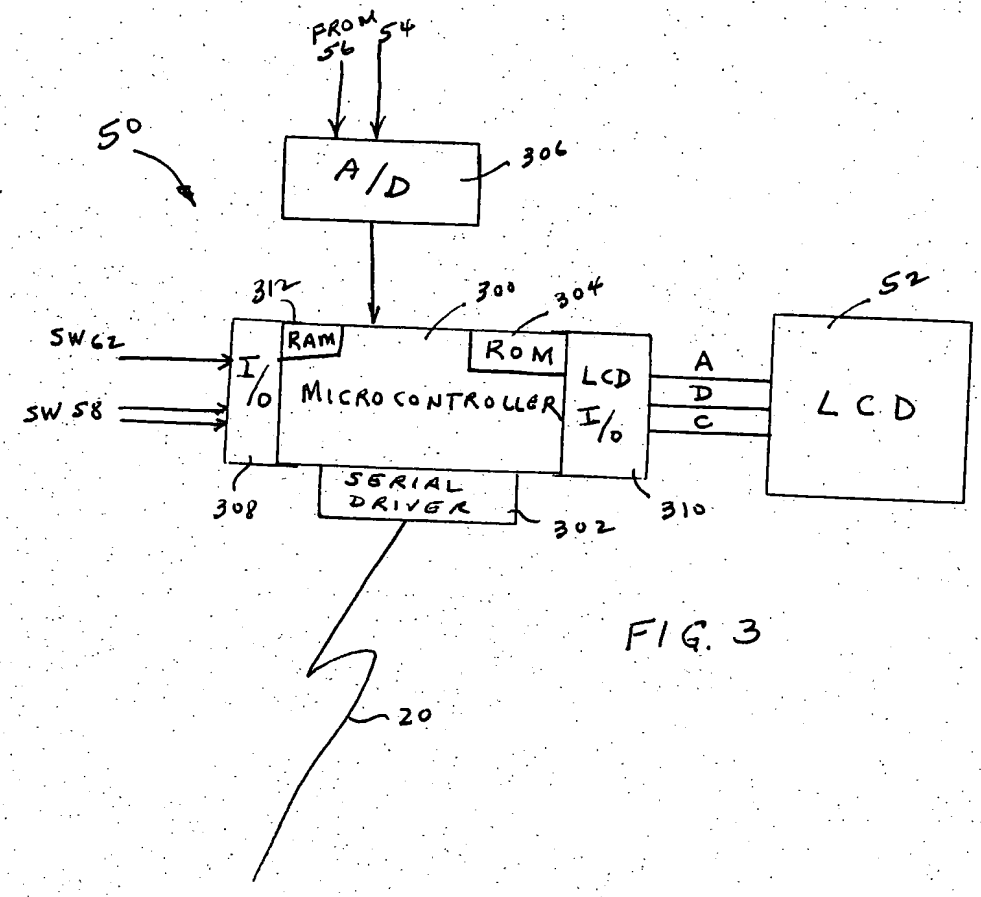
FROM 56
54
50
A/D
306
312
300
304
52
SW62
SW58
I/O
RAM
ROM
MICROCONTROLLER
LCD I/O
A
D
C
LCD
SERIAL DRIVER
308
302
310
20

FIG. 3

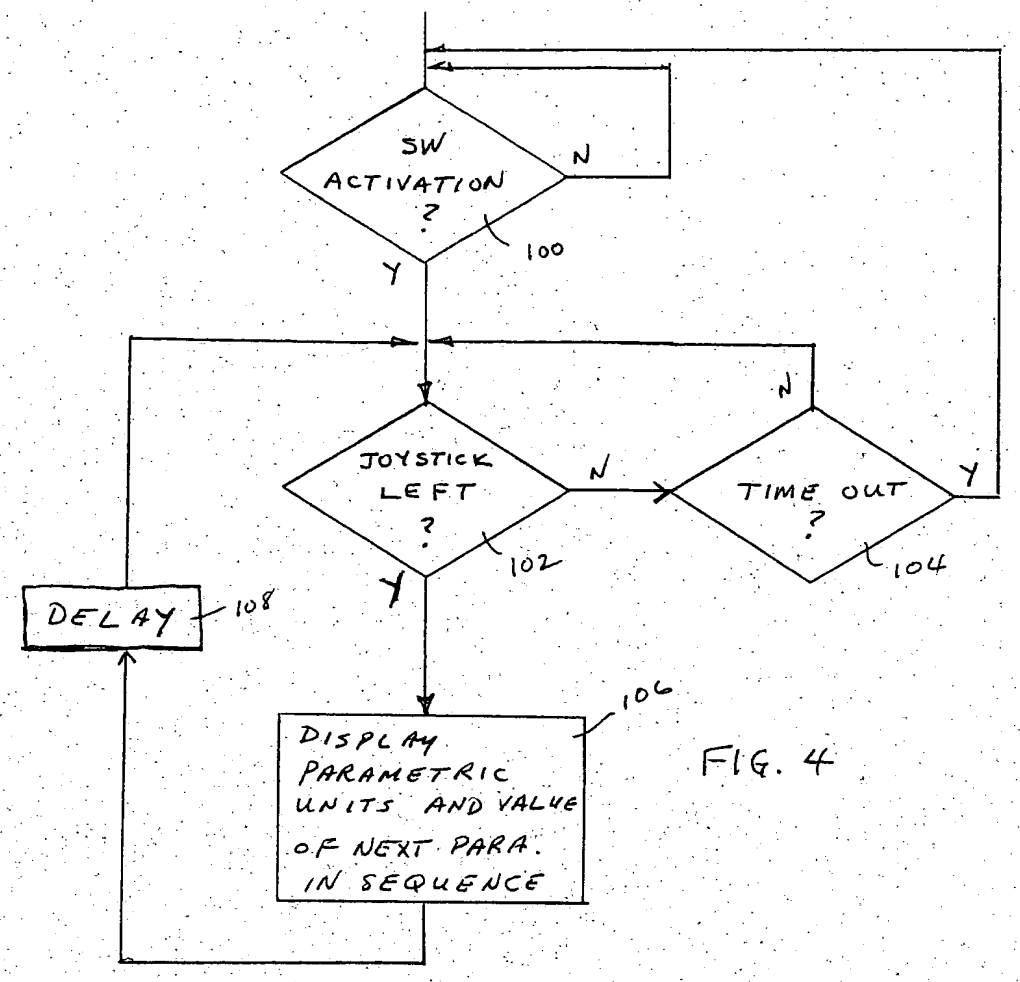
SW ACTIVATION ?
N
Y
100
JOYSTICK LEFT ?
N
Y
102
TIME OUT ?
N
Y
104
DELAY
108
DISPLAY PARAMETRIC UNITS AND VALUE OF NEXT PARA. IN SEQUENCE
106

FIG. 4

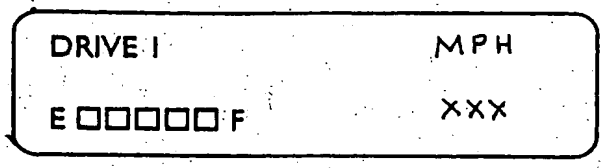

FIG. 5A

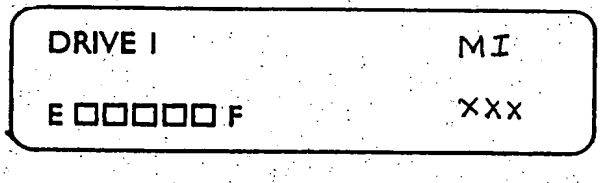

FIG. 5B

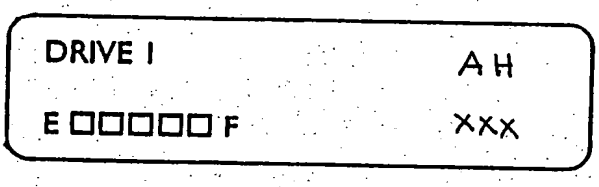

FIG. 5C

FIG. 5D

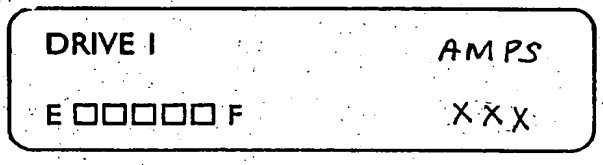

FIG. 5E

FIG. 5F